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Parson 3-2-1-4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): D.E. Parson et al.
Case: 3-2-1-4
Serial No.: 09/583,057
Filing Date: May 30, 2000
Group: 2184
Examiner: Timothy M. Bonura

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: V. Brennan Date: December 10, 2003

Title: Control Method and Apparatus for Testing of Multiple Processor Integrated Circuits and Other Digital Systems

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RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following remarks are submitted in response to the second non-final Office Action dated September 10, 2003 in the above-identified application. A Notice of Appeal is submitted concurrently herewith.

REMARKS

The present application was filed on May 30, 2000 with claims 1-20. Claims 1-20 remain pending. Claims 1, 13, 15, 19 and 20 are the independent claims.

Applicants respectfully request reconsideration of the present application in view of the remarks below.

Claims 1, 10, 11, 13, 14, 19 and 20 stand rejected under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 5,253,359 (hereinafter "Spix") in view of U.S. Patent No.